

Errata for FM25256 & FM25L256

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Errata Number 004 (all Date Codes)
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Product Marking FM25256 and FM25L256 (all package types)

Recent testing has identified a problem with FM25256 and FM25L256 devices that relates to the WEL bit in the status register. Writes are enabled when the WEL bit is set by sending the WREN op-code. However, if the RDSR op-code is sent following the WREN op-code, the WEL bit may be inadvertently cleared due to an internal fault condition. This prevents all writes to the device. To avoid this problem, do not send the RDSR op-code between the WREN and WRITE op-codes.

Patterns that may fail to write		Patterns that allow writes
1. WREN		1. WREN
2. RDSR		2. WRITE
3. WRITE		
1. WREN		1. WREN
2. RDSR		2. WRSR
3. WRSR		
		1. WREN
		2. READ
		3. WRITE
		1. WREN
		2. READ
		3. WRSR

NOTE: The above issue will be resolved with the next die revision. Samples will be available in 3Q06.