

# Errata for FM25L256

## RAMTRON

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**Errata Number**            005 (rev. AB, Date Codes 0410 thru 0430, excluding 0422)  
**Date**                        August 2, 2004  
**Product Marking**        FM25L256 (all package types)

Initial testing has identified some problems with FM25L256 samples.

- 1) Devices do not function properly under low temperature (below 0°C) and low voltage conditions. At 0°C and above, devices work over the entire  $V_{DD}$  range, 2.7V to 3.6V. However, between -25°C and 0°C,  $V_{DD}$  must be > 2.8V. Below -25°C,  $V_{DD}$  must be > 3.0V.
- 2) When the /HOLD pin is asserted low, internal operations are suspended but the SO pin is driven instead of hi-Z.
- 3) The required Deselect Time  $t_D$  is 150ns (min.) instead of 60ns (min.).
- 4) The Standby Current  $I_{SB}$  can be as high as 10 $\mu$ A. The spec limit is 5 $\mu$ A.
- 5) Data integrity is not guaranteed if all three of the following conditions occur: (1) a write is interrupted after the 5<sup>th</sup> and before the 8<sup>th</sup> clock of the data word, (2) and is then followed by a read, and (3) if  $t_D$  (/CS high time) is less than 200 ns.
- 6) Do not power down the device with /CS low during a write cycle after the 5<sup>th</sup> and before the 8<sup>th</sup> clock of the data word.

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NOTE: The above issues will be resolved with a rev. AE device. Samples will be available in early 4Q04.