

# Errata for FM25L256

## RAMTRON

1850 Ramtron Drive  
Colorado Springs, CO 80921  
719-481-7000 FAX: 719-488-9095

<b>Errata Number</b>	002 (rev. AC, Date Code 0422)
<b>Date</b>	August 2, 2004
<b>Product Marking</b>	FM25L256 (all package types)

Initial testing has identified some problems with FM25L256 Rev AC samples.

- 1) When the /HOLD pin is asserted low, internal operations are suspended but the SO pin is driven instead of hi-Z.
- 2) The required Deselect Time  $t_D$  is 150ns (min.) instead of 60ns (min.).
- 3) The Standby Current  $I_{SB}$  can be as high as 10 $\mu$ A. The spec limit is 5 $\mu$ A.
- 4) Data integrity is not guaranteed if all three of the following conditions occur: (1) a write is interrupted after the 5<sup>th</sup> and before the 8<sup>th</sup> clock of the data word, (2) and is then followed by a read, and (3) if  $t_D$  (/CS high time) is less than 280 ns.
- 5) Do not power down the device with /CS low during a write cycle after the 5<sup>th</sup> and before the 8<sup>th</sup> clock of the data word.

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NOTE: The above issues will be resolved with a rev. AE device. Samples will be available in early 4Q04.