

Errata for FM31x

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Errata Number 002 (rev. AC, Date Codes 0350 and up)

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Product Marking FM3104, FM3116, FM3164, FM31256

Initial testing has identified a few problems with the FM31x family of devices.

#1	Problem:	The VTP(1:0) bits that control the low-VDD reset threshold, are not fully operational. The VTP threshold is 2.6V. If the user programs to a value other than 00b, the chip cannot be accessed.
	Fix:	At power up, this should have little effect on the system since the reset signal will remain active for 100-200 ms even after VDD rises above the VTP threshold. Most systems will have reached a stable VDD level long before the reset signal is deasserted. At power down, the reset signal will not be asserted until VDD reaches 2.6V. <u>Do not attempt to program the VTP bits to a non-zero value.</u> From the factory, the VTP bits have been set to 00b, i.e. VTP1=0, VTP0=0 at address 0Bh.
#2	Problem:	I _{BAK} current is about 1.5 μ A instead of 1 μ A as specified (when the oscillator is enabled).
	Fix:	
#3	Problem:	I _{SB} current is greater than 150 μ A and standby current is unstable, only if the oscillator is not enabled.
	Fix:	Enable the oscillator by programming the /OSCEN bit to 0, even if a crystal is not used. It is recommended that /OSCEN=0 is programmed promptly after V _{DD} power is applied.

NOTE: The above issues will be resolved with a rev. AD device. Samples will be available in Jan. '04.