

Platform allows evaluation of FRAM devices



Supporting the VRS51L2070, the VersaKit-20xx evaluation platform comprises a set of probe vias and header footprints, an RS-232 transceiver, two DB9 connectors for UART access, and eight user LEDs. The device also includes two switches for manual reset/interrupt triggering, prototyping space, and JTAG access for in-circuit programming and real-time debugging of the VRS51L2070. An FM31256 processor companion, an FM25CL64 serial SPI FRAM (ferroelectric RAM), and an FM24CL64 serial I²C FRAM on the board allow evaluation of the vendor's FRAM devices. The VersaKit-20 costs \$99, including a development board, the Versa-JTAG programming/debugging interface, a DB25 parallel cable, and a power supply.

Ramtron International, www.ramtron.com